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a driver circuit comprising thin film transistors on said base film, each of said thin film transistors comprising a semiconductor layer and a gate electrode with a gate insulating film interposed therebetween;

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a passivation film covering said driver circuit, said passivation film having a contact hole to allow an electrical connection between at least one of said thin film transistors and said pixel circuit;

a wiring formed over said passivation film to form said electrical connection; and

a sealing member over said substrate, wherein said sealing member encloses said pixel circuit and said driver circuit.

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72. (Amended) An active matrix type display device comprising:

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a pixel circuit formed over a substrate for switching pixels of said display device;

a resin formed on said substrate;

a base film in contact with said resin;

a driver circuit comprising thin film transistors on said base film, each of said thin film transistors comprising a

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semiconductor layer and a gate electrode with a gate insulating film interposed therebetween;

an insulating film formed over said driver circuit;

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and

a wiring formed over said insulating film, wherein said driver circuit is electrically connected to said pixel circuit through said wiring.

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78. (Amended) An active matrix type display device comprising:

a pixel circuit formed over a substrate for switching pixels of said display device;

a resin formed on said substrate;

a base film in contact with said resin;

a driver circuit comprising thin film transistors on said base film, each of said thin film transistors comprising a semiconductor layer and a gate electrode with a gate insulating film interposed therebetween;

an insulating film formed over said driver circuit;

and

a wiring comprising indium tin oxide formed over said insulating film, wherein said driver circuit is electrically connected to said pixel circuit through said wiring.

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84. (Amended) An active matrix type display device comprising:

a pixel circuit formed over a substrate for switching pixels of said display device;

a resin formed on said substrate;

a base film in contact with said resin;

a driver circuit comprising thin film transistors on said base film, each of said thin film transistors comprising a semiconductor layer and a gate electrode with a gate insulating film interposed therebetween;

an insulating film formed over said driver circuit;

and

a wiring comprising indium tin oxide formed over said insulating film and extending on a same layer as said resin, wherein said driver circuit is electrically connected to said pixel circuit through said wiring.

90. (Amended) An active matrix type display device comprising:

a pixel circuit formed over a substrate for switching pixels of said display device;

a resin formed over said substrate;

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cont

a base film in contact with said resin;  
a semiconductor layer on said base film;  
a gate electrode over said semiconductor layer with a  
gate insulating film interposed therebetween;

at least one of source and drain electrodes  
electrically connected to said semiconductor layer;

an insulating film formed over at least said  
semiconductor layer and said gate electrode; and

a wiring formed over said insulating film, wherein  
said at least one of said source and drain electrodes is  
electrically connected to said pixel circuit through said  
wiring.

95. (Amended) An active matrix type display device  
comprising:

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a pixel circuit formed over a substrate for switching  
pixels of said display device;

a resin formed over said substrate;

a base film in contact with said resin;

a semiconductor layer on said base film;

a gate electrode over said semiconductor layer with a  
gate insulating film interposed therebetween;

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at least one of source and drain electrodes  
electrically connected to said semiconductor layer;

an insulating film formed over at least said  
semiconductor layer and said gate electrode; and

a wiring comprising indium tin oxide formed over said  
insulating film, wherein said at least one of said source and  
drain electrodes is electrically connected to said pixel circuit  
through said wiring.

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100. (Amended) An active matrix type display device  
comprising:

a pixel circuit formed over a substrate for switching  
pixels of said display device;

a resin formed over said substrate;

a base film in contact with said resin;

a semiconductor layer on said base film;

a gate electrode over said semiconductor layer with a  
gate insulating film interposed therebetween;

at least one of source and drain electrodes  
electrically connected to said semiconductor layer;

an insulating film formed over at least said  
semiconductor layer and said gate electrode; and

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G7  
can  
a wiring comprising indium tin oxide formed over said  
insulating film and extending on a same layer as said resin,  
wherein said at least one of said source and drain electrodes is  
electrically connected to said pixel circuit through said  
wiring.--

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